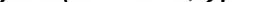


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DT15 Rec. 07/PTC 20 JUL 2004

<p>Substitute for form 1449A/B/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p><i>(Use as many sheets as necessary)</i></p>				<p><i>Complete if Known</i></p> <table border="1"> <tr> <td>Application Number</td> <td>Unassigned</td> <td>10/501883</td> </tr> <tr> <td>Filing Date</td> <td>July 20, 2004</td> <td></td> </tr> <tr> <td>First Named Inventor</td> <td colspan="2">Yasushi HASHIZUME et al.</td> </tr> <tr> <td>Group Art Unit</td> <td colspan="2">Unassigned</td> </tr> <tr> <td>Examiner Name</td> <td colspan="2">Unassigned</td> </tr> <tr> <td colspan="3">Attorney Docket Number</td> <td>403137/SHINSEI</td> </tr> </table>		Application Number	Unassigned	10/501883	Filing Date	July 20, 2004		First Named Inventor	Yasushi HASHIZUME et al.		Group Art Unit	Unassigned		Examiner Name	Unassigned		Attorney Docket Number			403137/SHINSEI
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Sheet	1	of	1																					

OTHER - NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Doc. No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number (s), publisher, city and/or country where published.	Translation	
			Yes	No ⁺⁺
JB		Joachim N. Burghartz, et al. "Microwave Inductors and Capacitors in Standard Multilevel Interconnect Silicon Technology", IEEE Transactions on Microwave Theory and Techniques, Vol. 44, No. 1, January 1996, pp. 100-104.		

Examiner Signature		Date Considered	8/8/15
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- A concise statement of relevance is being submitted in lieu of a translation. 37 CFR 1.98(a)(3).
- An English-language equivalent/patent, or an English-language abstract, or an English-language version of the search report or action by a foreign patent office in a counterpart foreign application indicating the degree of relevance found by the foreign office is being submitted in lieu of a concise explanation of relevance under 37 CFR 1.98(a)(3).